

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|---|---|------------------|---------|------------------|
| L3 | 22 | ((cam or content addressable memory) with (match flag)) same (select\$4 near8 (format or output)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/07 09:22 |
| S1 | 9 | 711/108 and altera.as. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 11:41 |
| S2 | 153 | 711/108 and ((byte or bit) same pattern) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/01 09:06 |
| S3 | 20 | 711/108 and ((byte or bit) same pattern same align or alignment) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/10/31 12:55 |
| S4 | 0 | 711/108 and ((byte or bit) same pattern same (align or alignment)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/10/31 12:56 |
| S5 | 124 | (cam or content addressable memory) and (deserializ\$4 or de-serializ\$4) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/10/31 12:57 |
| S6 | 105 | (cam or content addressable memory) and (deserializ\$4 or de-serializ\$4) and parallel | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/10/31 12:58 |
| S7 | 84 | (cam or content addressable memory) and (deserializ\$4 or de-serializ\$4) and parallel and register | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 14:59 |

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| S8 | 62 | (cam or content addressable memory) and (deserializ\$4 or de-serializ\$4) and parallel and register and pattern | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/10/31 12:59 |
| S9 | 24 | 711/108 and ((byte or bit) with align or alignment) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/01 09:06 |
| S10 | 16 | 711/108 and ((byte or bit) with (align or alignment)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/01 09:06 |
| S12 | 329 | 370/366 | US-PGPUB; USPAT | ADJ | ON | 2005/11/03 11:43 |
| S13 | 10 | 370/366 and (cam or content address\$4 memory) | US-PGPUB; USPAT | ADJ | ON | 2005/11/03 11:46 |
| S15 | 521 | (byte near3 align\$4) and (cam or content address\$4 memory) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 11:47 |
| S16 | 213 | (byte near3 align\$4) and (cam or content address\$4 memory) and (serial same parallel) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 11:48 |
| S17 | 149 | (byte near3 align\$4) and (cam or content address\$4 memory) and (serial with parallel) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 11:48 |
| S18 | 0 | (byte near3 align\$4) same (cam or content address\$4 memory) same (serial with parallel) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 11:48 |
| S19 | 309 | ((byte or bit) near3 align\$4) and (cam or content address\$4 memory) and (serial with parallel) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 11:49 |

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| S20 | 110 | ((byte or bit) near3 align\$4) and (cam or content address\$4 memory) and ((deserializ\$3 or de-serializ\$3 or convert\$3) with serial with parallel) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 12:07 |
| S21 | 0 | ((byte or bit) near3 align\$4) same ((deserializ\$3 or de-serializ\$3 or convert\$3) with serial with parallel) and (cam or content address\$4 memory) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 11:51 |
| S22 | 4 | (cam or content address\$4 memory) and ((multiple or plurality) near5 parallel near5 format) and (shift register) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 12:09 |
| S23 | 0 | (cam or content address\$4 memory) with ((multiple or plurality) adj5 parallel adj5 format) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 12:10 |
| S24 | 3170 | (parallel adj5 bits) with shift register | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 12:11 |
| S25 | 94 | ((deserializ\$3 or de-serializ\$3 or convert\$3) with serial) same ((byte or bit) with (align\$4 near8 parallel)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 12:13 |
| S26 | 0 | ((deserializ\$3 or de-serializ\$3 or convert\$3) with serial) same ((byte or bit) with (align\$4 near8 parallel)) and (CAM or content addressable memory) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 12:13 |
| S27 | 41 | ((deserializ\$3 or de-serializ\$3 or convert\$3) with serial) same ((byte or bit) with (align\$4 near8 parallel)) and (shift register) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 12:46 |

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| S28 | 0 | S27 and (CAM or content addressable memory) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 12:28 |
| S29 | 0 | ((deserializ\$3 or de-serializ\$3 or convert\$3) with serial) same ((byte or bit) with (align\$4 near8 parallel)) and (cam or content addressable memory) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 12:44 |
| S30 | 0 | ((deserializ\$3 or de-serializ\$3 or convert\$3) with serial) same ((byte or bit) with (align\$4 near8 parallel)) and (associative memory) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 12:45 |
| S31 | 260 | (cam or content addressable memory or associative memory) with (shift register) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 12:47 |
| S32 | 17 | (cam or content addressable memory or associative memory) with (shift register) and ((deserializ\$3 or de-serializ\$3 or convert\$3) with serial) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 14:27 |
| S33 | 1199 | 710/64 or 710/65 or 710/71 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 13:44 |
| S35 | 29 | (cam or content addressable memory) and S33 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 13:46 |
| S36 | 24 | (cam or content addressable memory) and S33 and (parallel or serial) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 13:46 |

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| S37 | 19 | (cam or content addressable memory) same shift register same (serial with parallel) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 14:20 |
| S38 | 0 | (cam or content addressable memory) same shift register same (deserializ\$4) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 14:20 |
| S39 | 4 | (cam or content addressable memory) same shift register same (serial near8 (convert\$3)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 14:24 |
| S40 | 1 | (cam or content addressable memory) same shifter same (serial near8 (convert\$3)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 14:24 |
| S41 | 2 | "5652878".pn. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 14:32 |
| S42 | 195 | (cam or content addressable memory) with shift register | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 14:32 |
| S43 | 17 | (cam or content addressable memory) same shift register same (deserializ\$4 or (serial near6 parallel)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 14:37 |
| S44 | 96 | (cam or content addressable memory) near20 shift register | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 14:37 |

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| S45 | 85 | (cam or content addressable memory) near10 shift register | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 14:40 |
| S46 | 1 | (cam or content addressable memory) with shift register with converter | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 14:41 |
| S47 | 23 | (cam or content addressable memory) same shift register same converter | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 14:41 |
| S48 | 17 | (cam or content addressable memory) same shift register same (serial near6 parallel) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 14:42 |
| S50 | 68 | parallel with (cam or content addressable memory) with register | USPAT | ADJ | ON | 2005/11/03 14:43 |
| S51 | 427 | parallel with (cam or content addressable memory) with register | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 14:43 |
| S52 | 4 | parallel with (cam or content addressable memory) with shift register | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 14:43 |
| S53 | 2861 | (cam or content addressable memory) and shift register | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 14:59 |
| S54 | 195 | (cam or content addressable memory) with shift register | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 15:00 |

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| S55 | 9928 | (parallel with serial) with shift register | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 15:00 |
| S56 | 15 | S54 and S55 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 15:01 |
| S57 | 544 | (cam or content addressable memory) and (shift near5 register) and (deserializ\$4 or de-serializ\$4 or (serial near3 parallel)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 15:02 |
| S58 | 1 | (cam or content addressable memory) with (shift near5 register) with (deserializ\$4 or de-serializ\$4 or (serial near3 parallel)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 15:03 |
| S59 | 17 | (cam or content addressable memory) same (shift near5 register) same (deserializ\$4 or de-serializ\$4 or (serial near3 parallel)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 17:51 |
| S60 | 0 | S59 not S48 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 15:03 |
| S61 | 21 | (cam or content addressable memory) same (shift near5 register) same (deserializ\$4 or de-serializ\$4 or (serial with parallel)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 15:05 |
| S62 | 2 | "3,648,254".pn. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 15:05 |

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| S64 | 3 | (shift register) adj8 input adj8 (cam or content addressable memory) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 15:14 |
| S65 | 719533 | (cam or content addressable memory) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 17:08 |
| S66 | 0 | (cam or content addressable memory) near30 (deserializ\$4 or ((serial near3 parallel) near4 convert\$3)) near30 (shift near3 register) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 17:10 |
| S67 | 1 | (cam or content addressable memory) near30 (deserializ\$4 or (serial near3 parallel)) near30 (shift near3 register) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 17:13 |
| S68 | 195 | (cam or content addressable memory) with shift register | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 17:14 |
| S69 | 49 | (cam or content addressable memory) with shift register and (serial same parallel) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 17:14 |
| S70 | 842 | (ram or random access memory) same (shift near5 register) same (deserializ\$4 or de-serializ\$4 or (serial near3 parallel)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 17:52 |
| S71 | 300 | (ram or random access memory) with (shift near5 register) with (deserializ\$4 or de-serializ\$4 or (serial near3 parallel)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 17:52 |

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| S72 | 296 | (ram or random access memory) with (shift register) with (deserializ\$4 or de-serializ\$4 or (serial near3 parallel)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 07:57 |
| S73 | 0 | (ram or random access memory) with (shift register) with (deserializ\$4 or de-serializ\$4 or (serial near3 parallel)) and (byte near4 align\$4) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 17:53 |
| S74 | 34 | (ram or random access memory) with (shift register) with (deserializ\$4 or de-serializ\$4 or (serial near3 parallel)) and ((byte or bit) with align\$4) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/03 17:57 |
| S75 | 21 | (ram or random access memory) with (shift register) with ((serial near5 parallel) near6 convert\$4) and ((byte or bit) with align\$4) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 07:13 |
| S76 | 31 | (memory) with (shift register) with ((serial near5 parallel) near6 convert\$4) and ((byte or bit) with align\$4) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 07:48 |
| S78 | 0 | (SONET and (cam or content addressable memory)).ab. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 07:49 |
| S79 | 34 | ((SONET or ethernet) and (cam or content addressable memory)).ab. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 07:52 |
| S81 | 0 | ((SONET or ethernet comma) and (cam or content addressable memory)).ab. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 07:49 |

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| S84 | 4 | (deserializ\$4 or (serial with parallel)) and S79 and shift register | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 07:54 |
| S85 | 19 | (stor\$4 adj3 (ram or random access memory)) with (shift register) with (deserializ\$4 or de-serializ\$4 or (serial near3 parallel)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 07:58 |
| S86 | 287 | align\$4 with (ic or integrated circuit) with (cam or content addressable memory) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 08:19 |
| S87 | 6 | (align\$4 near6 (unit or device)) with (ic or integrated circuit) with (cam or content addressable memory) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 08:14 |
| S88 | 0 | (sonet with (ic or integrated circuit)) with (cam or content addressable memory) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 08:15 |
| S89 | 18731 | (ic or integrated circuit) with (cam or content addressable memory) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 08:21 |
| S90 | 31 | (align\$4 with parallel) with (ic or integrated circuit) with (cam or content addressable memory) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 08:19 |
| S91 | 1 | (align\$4 with data) with (ic or integrated circuit) with (cam or content addressable memory) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 08:19 |



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| S92 | 8314 | align\$4 with (cam or content addressable memory) and (ic or integrated circuit) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 08:20 |
| S93 | 138 | (align\$4 near4 parallel) with (cam or content addressable memory) and (ic or integrated circuit) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 08:20 |
| S94 | 200 | ((programmable near4 (ic or integrated circuit)) or FPGA or PGA) with (cam or content addressable memory) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 08:58 |
| S95 | 19 | sonet and (tristate or tri-state) buffer | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 09:05 |
| S96 | 6124 | (mux or multiplexer) with shift register | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 09:05 |
| S97 | 17 | (mux or multiplexer) with shift register with deserializ\$4 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 09:08 |
| S98 | 336 | (mux or multiplexer) with shift register with (serial near6 parallel) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 09:08 |
| S99 | 33 | ((first and second) with (mux or multiplexer)) with shift register with (serial near6 parallel) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 09:18 |

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| S10 0 | 123 | shift register adj10 (compos\$3 or compris\$3 or contain\$3 or includ\$3) adj6 (mux or multiplexer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 09:19 |
| S10 1 | 116 | shift register adj10 (compos\$3 or compris\$3 or contain\$3 or includ\$3) adj6 ((first and second) near4 mux or multiplexer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 09:19 |
| S10 2 | 9 | shift register adj10 (compos\$3 or compris\$3 or contain\$3 or includ\$3) adj6 ((first and second) near4 (mux or multiplexer)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 09:39 |
| S10 3 | 58 | shift register adj3 (compos\$3 or compris\$3 or contain\$3 or includ\$3) adj3 (mux or multiplexer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 09:32 |
| S10 4 | 640 | (shift register).ti. and (mux or multiplexer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 09:32 |
| S10 5 | 126 | (shift register).ti. and ((compris\$3 or compos\$3 or includ\$4 or contain\$4) near6 (mux or multiplexer)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 09:33 |
| S10 6 | 87 | shift register adj5 (compos\$3 or compris\$3 or contain\$3 or includ\$3) adj5 (mux or multiplexer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 09:46 |
| S10 7 | 0 | deserializ\$4 with (shift register adj5 (compos\$3 or compris\$3 or contain\$3 or includ\$3) adj5 (mux or multiplexer)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 09:47 |

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| S10 8 | 551 | shift register with ((first and second) near10 (mux or multiplexer)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 09:48 |
| S10 9 | 188 | shift register with (contain\$4 or compris\$4 or includ\$4 or compos\$4) with ((first and second) near10 (mux or multiplexer)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 09:58 |
| S11 0 | 221 | stage near6 shift register near6 (mux or multiplexer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 09:59 |
| S11 1 | 88 | stage adj4 shift register adj4 (mux or multiplexer) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 09:59 |
| S11 3 | 304 | (cam or content addressable memory) with match (bit or flag) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 11:31 |
| S11 4 | 179 | (cam or content addressable memory) with (match flag) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/04 11:34 |
| S11 5 | 73 | (cam or content addressable memory) with (match flag) with register | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | ADJ | ON | 2005/11/07 09:22 |